



## INFORMATION DISCLOSURE CITATION

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## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date if Appropriate
<i>JK</i>	5,864,243	01/26/1999	Chen et al.	326	58	

## FOREIGN PATENT DOCUMENTS

Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No

## OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

<i>JK</i>	Marcel J. M. Pelgrom and E. Carel Dijkmans, "A 3/5 V Compatible I/O Buffer," IEEE Journal of Solid-State Circuits, vol. 30, no. 7, July 1995, pp. 823-825
<i>JK</i>	M. Takahashi, T. Sakurai, K. Sawada, K. Nogami, M. Ichida, and K. Matsuda, "3.3V-5V Compatible I/O Circuit Without Thick Gate Oxide," Proc. of IEEE Custom Integrated Circuits Conference, 1992, pp. 23.3.1-23.3.4
<i>JK</i>	G. Singh and R. Salem, "High-Voltage Tolerant I/O Buffers With Low-Voltage CMOS Process," IEEE Journal of Solid-State Circuits, vol. 34, no. 11, 1999, pp. 1512-1525 <i>November 1999</i>
<i>JK</i>	Deng-Yuan Chen, "Design of a Mixed 3.3V and 5V PCI I/O Buffer," Proc. of ASIC, 1996, pp. 336-339 <i>Nov 1996</i>

Examiner <i>James Odo</i>	Date Considered <i>12/6/04</i>
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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